Table 1

	Lecture Section	Lecture Detail	Assignment Start	Assignment Due	Notes
1/29	Syllabus				
1/31	Computer Arithmetic				
2/5	Digital Design	Motivation, Set Theory, Truth Table	HW1		
2/7		SOP/POS, Gates, Timing diagram			
2/12		K-Maps	HW2	HW1	
2/14		Sequential Logic, Clocks			
2/19		FSM, Memory		HW2	
2/21	Comp Org & Assembly	Motivation, Building blocks	HW3		
2/26		Fetch, Control	Quiz1	Quiz1	
2/28		Pipeline	HW4	HW3	
3/4		X86 intro, registers, data types			
3/6		Execution and Debug	Proj1	HW4	
3/11		Review			
3/13		Midterm exam			
Spring Break					
3/25		Recap			
3/27		X86 program flow	Proj2	Proj1	
4/1		X86 loops, addressing			
4/3		Hazards			
4/8		Dependency			
4/15		Function Call			
4/17		Memory	Proj3	Proj2	
4/22		Interrupt	Quiz2	Quiz2	
4/24		Parallelism			
4/29		Summary			
5/1	С	Overview, Compiler	Proj4	Proj3	
5/6		Low-level operations			
5/8		Summary			
5/13		Review		Proj4	Last day of class
5/20		Final exam			10:30am-12:30pm
	Schedule subject to change				

1/29		
1/31		
2/5		
2/7		
2/12		
2/14		
2/19		
2/21		
2/26		
2/28		
3/4		
3/6		
3/11		
3/13		
Spring Break		
3/25		
3/27		
4/1		
4/3		
4/8		
4/15		
4/17		
4/22		
4/24		
4/29		
5/1		
5/6		
5/8		
5/13		
5/20		